

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 06-004679

(43)Date of publication of application : 14.01.1994

(51)Int.Cl.

G06F 15/72 G09G 5/36

(21)Application number : 04-166189

(71)Applicant : TOSHIBA CORP

(22)Date of filing : 24.06.1992

(72)Inventor : KAI NAOYUKI

NAGASHIMA ICHIRO

(54) IMAGE PROCESSOR

(57)Abstract:

PURPOSE: To provide an image processor which can plot a polygon that is subjected to anti-aliasing at high speed with less hardware amount by simultaneously executing an anti-aliasing processing and the judgement processing of paint out area on an image processor which efficiently removes the aliasing of a boundary in the paint out or shading of the polygon.

CONSTITUTION: A calculation means 1 which sequentially calculates the linear expression of a two-dimensional coordinate system in accordance with the respective sides of the polygon, judges a relative position relation of plotting points with respect to the polygon and obtains the displacement of the respective plotting points from the sides of the polygon and a conversion means 5 changing the color or gradation of the plotting point near the sides of the polygon based on displacement obtained in

the calculation means 1 are provided. Then, the internal part of the polygon is painted out or it is shaded on a bit map memory.

